

## AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

### Listing of Claims:

- 1           1. (Currently amended) An apparatus that facilitates communication  
2 | between integrated circuits with an integrated circuit device within a computing  
3 system, comprising:  
4 |       ~~the a first integrated circuit within the computing system device;~~  
5       a first radio port coupled to the first integrated circuit ~~device~~, wherein the  
6 first radio port includes a first transmitting mechanism that is configured to  
7 generate a radio signal in response to a first command from the first integrated  
8 circuit ~~device~~;  
9       ~~an antenna coupled to the radio port, wherein the antenna is configured to~~  
10 ~~transmit the radio signal generated by the transmitting mechanism, and wherein~~  
11 ~~the antenna is additionally configured to detect a response to the radio signal; and~~  
12       wherein the first radio port further includes a first receiving mechanism,  
13 wherein the first receiving mechanism is configured to receive ~~the response from~~  
14 ~~the antenna and pass the response to a radio signal for the first integrated circuit~~  
15 ~~device;~~  
16       a second integrated circuit within the computing system; and  
17       a second radio port coupled to the second integrated circuit, wherein the  
18 second radio port includes a second transmitting mechanism that is configured to  
19 generate a radio signal in response to a second command from the second  
20 integrated circuit;

21        wherein the second radio port further includes a second receiving  
22        mechanism, wherein the second receiving mechanism is configured to receive a  
23        radio signal for the second integrated circuit; and  
24        wherein the first radio port is configured to communicate with the second  
25        radio port, whereby the first integrated circuit within the computing system can  
26        communicate with the second integrated circuit within a same computer system  
27        through radio signals without using normal bus structures of the same computer  
28        system.

1            2. (Currently amended) The apparatus of claim 1, wherein communication  
2        with-between the first integrated circuit and the second integrated circuit device  
3        includes communication of one of, boundary-scan data, initialization information,  
4        identification information, configuration information, results of self-tests, and  
5        error reports.

1            3. (Currently amended) The apparatus of claim 1, wherein the first radio  
2        port is implemented in a separate integrated circuit ~~device~~.

1            4. (Currently amended) The apparatus of claim 1, wherein the first radio  
2        port is incorporated into the first integrated circuit ~~device~~.

1            5. (Currently amended) The apparatus of claim 4, wherein the first radio  
2        port receives operating power from the first integrated circuit's device's power  
3        supply.

1            6. (Currently amended) The apparatus of claim 4, wherein the first radio  
2        port receives operating power from a battery.

1           7. (Currently amended) The apparatus of claim 4, wherein the first radio  
2 port receives operating power from radio waves received on ~~the~~ an antenna.

1           8. (Currently amended) The apparatus of claim 4, wherein ~~the~~ an antenna  
2 is incorporated into the first integrated circuit ~~device~~.

1           9. (Currently amended) The apparatus of claim 4, wherein ~~the~~ an antenna  
2 is a trace on a printed-wire board.

1           10. (Currently amended) The apparatus of claim 4, wherein ~~the~~ an antenna  
2 is a separate wire.

1           11. (Currently amended) The apparatus of claim 1, wherein the first radio  
2 port includes a collision detection mechanism that is configured to detect a  
3 collision when more than one response is received simultaneously.

1           12. (Currently amended) The apparatus of claim 11, wherein the first radio  
2 port includes a collision recovery mechanism that is configured to resolve  
3 collisions when more than one response is received simultaneously.

1           13. (Currently amended) An apparatus that facilitates communication  
2 between integrated circuits ~~with an integrated circuit device~~ within a computing  
3 system, comprising:

4           ~~the~~ a first integrated circuit within the computing system ~~device~~;

5           a first radio port coupled to the first integrated circuit ~~device~~;

6           an antenna coupled to the radio port;

7           ~~wherein the antenna is configured to detect a radio signal and pass the~~  
8 ~~radio signal to the radio port;~~

9            wherein the first radio port includes a first receiving mechanism that is  
10 configured to receive ~~the a~~ radio signal ~~from the antenna~~ for the first integrated  
11 circuit;  
12            wherein the first radio port includes a first passing mechanism that is  
13 configured to pass control commands to the first integrated circuit ~~device~~ in  
14 response to the radio signal; and  
15            wherein the first radio port further includes a first transmitting mechanism  
16 that is configured to transmit a response to the radio signal that is generated by the  
17 first integrated circuit device;  
18            a second integrated circuit within the computing system; and  
19            a second radio port coupled to the second integrated circuit;  
20            wherein the second radio port includes a second receiving mechanism that  
21 is configured to receive a radio signal for the second integrated circuit;  
22            wherein the second radio port includes a second passing mechanism that is  
23 configured to pass control commands to the second integrated circuit in response  
24 to the radio signal; and  
25            wherein the second radio port further includes a second transmitting  
26 mechanism that is configured to transmit a response to the radio signal that is  
27 generated by the second integrated circuit;  
28            whereby the first integrated circuit within the computing system can  
29 communicate with the second integrated circuit within a same computer system  
30 through radio signals without using normal bus structures of the same computer  
31 system.

1            14. (Currently amended) The apparatus of claim 13, wherein  
2 communication with the first integrated circuit ~~device~~ includes communication  
3 and monitoring of boundary-scan data, self test data, power and temperature data,  
4 chip identification data, and configuration data.

1           15. (Currently amended) The apparatus of claim 13, wherein the first radio  
2 port is incorporated into the first integrated circuit ~~device~~.

1           16. (Currently amended) The apparatus of claim 15, wherein the first radio  
2 port receives operating power from the first integrated circuit's ~~device's~~ power  
3 supply.

1           17. (Currently amended) The apparatus of claim 15, wherein the first radio  
2 port receives operating power from a battery.

1           18. (Currently amended) The apparatus of claim 15, wherein the first radio  
2 port receives operating power from radio waves received on the antenna.

1           19. (Currently amended) The apparatus of claim 15, wherein ~~the~~ an  
2 antenna is incorporated into the integrated circuit ~~device~~.

1           20-31 (Canceled).

1           32. (Currently amended) An apparatus that facilitates communication  
2 between integrated circuits ~~with an integrated circuit device~~ within a computing  
3 subsystem within a computing system, wherein the computing subsystem is  
4 separated from other computing subsystems within the computing system,  
5 comprising:  
6           the computing subsystem including ~~the~~ a first integrated circuit ~~device~~;  
7           a first radio port coupled to the first integrated circuit ~~device~~, wherein the  
8 first radio port includes a first transmitting mechanism that is configured to  
9 generate a radio signal in response to a first command from the first integrated  
10 circuit ~~device~~;

11 ~~an~~ a first antenna coupled to the first radio port external to the computing  
12 subsystem, wherein the first antenna is configured to transmit the first radio signal  
13 generated by the first transmitting mechanism, and wherein the first antenna is  
14 additionally configured to detect a first response to the radio signal; and  
15 wherein the first radio port further includes a first receiving mechanism,  
16 wherein the first receiving mechanism is configured to receive the first response  
17 from the first antenna and pass the first response to the first integrated circuit  
18 device;  
19 wherein the computing subsystem includes a second integrated circuit;  
20 a second radio port coupled to the second integrated circuit, wherein the  
21 second radio port includes a second transmitting mechanism that is configured to  
22 generate a radio signal in response to a second command from the second  
23 integrated circuit;  
24 a second antenna coupled to the second radio port external to the  
25 computing subsystem, wherein the second antenna is configured to transmit the  
26 second radio signal generated by the second transmitting mechanism, and wherein  
27 the second antenna is additionally configured to detect a second response to the  
28 radio signal; and  
29 wherein the second radio port further includes a second receiving  
30 mechanism, wherein the second receiving mechanism is configured to receive the  
31 second response from the second antenna and pass the second response to the  
32 second integrated circuit.